FIRST SEMESTER

Slot	Code	Subject	Hours/Week			Internal Marks	End Semester Examination		Credit
			L	T	P		Hrs	Marks	
A	10EC6101	Linear Algebra	3	1	-	40	3	60	4
B	10EC6103	Random Processes and Applications	3	-	-	40	3	60	3
C	10EC6105	Advanced Digital Signal Processing	3	-	-	40	3	60	3
D	10EC6205	Advanced Embedded Processors	3	-	-	40	3	60	3
E		Elective-I	3	-	-	40	3	60	3
S	10GN6001	Research Methodology	-	2	-	100	-	0	2
T	10EC6309	Seminar-1	-	-	2	100	-	0	2
U	10EC6111	Digital Signal Processing Laboratory	-	-	2	100	-	0	1
	TOTAL		**15**	**3**	**4**	**500**		**300**	**21**

ELECTIVE-I

10EC6401 Multi-rate Signal Processing
10EC6113 DSP Processors and Architecture
10EC6203 CMOS VLSI Design
10EC6201 High Speed Digital Design
10EC6313 Design of Analog MOS ICs
10EC6119 Transform Theory

SECOND SEMESTER

Slot	Code	Subject	Hours/Week			Internal Marks	End Semester Examination		Credit
			L	T	P		Hrs	Marks	
A	10EC6102	Digital Image Processing	3	-	-	40	3	60	3
B	10EC6302	Wavelet Theory	3	-	-	40	3	60	3
C	10EC6304	Embedded System Design	3	-	-	40	3	60	3
D		Elective-II	3	-	-	40	3	60	3
E		Elective-III	3	-	-	40	3	60	3
V	10EC6308	Mini Project	-	-	4	100	-	0	2
U	10EC6312	VLSI & Embedded Systems Laboratory	-	-	2	100	-	0	1
	TOTAL		**15**	**0**	**6**	**400**		**300**	**18**

ELECTIVE-II

10EC6404 Adaptive Signal Processing
10EC6114 Biomedical Signal Processing
10EC6202 Advanced Digital System Design
10EC6314 Optical Signal Processing
10EC6218 VLSI System Design

ELECTIVE-III

10EC6402 VLSI Signal Processing
10EC6118 Statistical Signal Processing
10EC6316 Multidimensional Signal Processing
10EC6318 Mixed Signal Circuit Design
10EC6216 Low Power VLSI Design

THIRD SEMESTER

Slot	Code	Subject	Hours/Week			Internal Marks	End Semester Examination		Credit
			L	T	P		Hrs	Marks	
A		Elective-IV	3	-	-	40	3	60	3
B		Elective-V	3	-	-	40	3	60	3
T	10EC7301	Seminar-2	-	-	2	100	-	0	2
W	10EC7303	Project - Phase 1	-	-	12	50	-	0	6
	TOTAL		**6**	**-**	**14**	**230**		**120**	**14**

ELECTIVE-IV

10EC7105 Audio Processing

10EC7205 Biometric Processing

10EC7109 Array Signal Processing

10EC7305 Computer Vision

10EC7209 Embedded Networks

ELECTIVE-V

10EC7113 Pattern Recognition

10EC7307 Multimedia Systems

10EC7117 Information Hiding and Data Encryption

10EC7215 ASIC Design

10EC7213 Introduction to Nano Electronics

FOURTH SEMESTER

Slot	Code	Subject	Hours/Week			Internal Marks	End Semester Examination		Credit
			L	T	P		Hrs	Marks	
W	10EC7304	Project - Phase 2	-	-	21	70	1	30	12
	TOTAL		**-**	**-**	**21**	**70**		**30**	**12**

Credits :

First Semester	: 21
Second Semester	: 18
Third Semester	: 14
Fourth Semester	: 12
Total	**: 65**